

GL Silicon N-Channel Power MOSFET

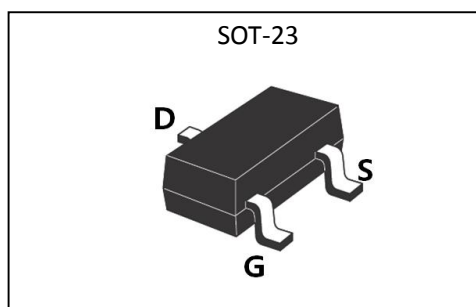
General Description :

The GL0102 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications. The package form is SOT-23, which accords with the RoHS standard.

| | | |
|------------------|------|------------|
| V_{DSS} | 100 | V |
| I_D | 2 | A |
| P_D | 1.25 | W |
| $R_{DS(ON)type}$ | 210 | m Ω |

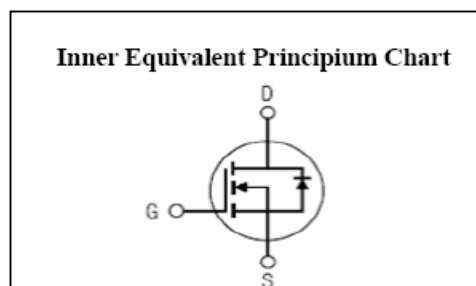
Features :

- $R_{DS(ON)} < 240m\Omega$ @ $V_{GS}=10V$ (Typ210m Ω)
- High density cell design for ultra low R_{dsn}
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation



Applications :

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Absolute ($T_c = 25^\circ C$ unless otherwise specified) :

| Symbol | Parameter | Rating | Units |
|----------------|--|------------------|------------|
| V_{DSS} | Drain-to-Source Voltage | 100 | V |
| I_D | Continuous Drain Current | 2 | A |
| I_{DM} | Pulsed Drain Current | 5 | A |
| V_{GS} | Gate-to-Source Voltage | ± 20 | V |
| P_D | Power Dissipation | 1.25 | W |
| T_J, T_{stg} | Operating Junction and Storage Temperature Range | 175 , -55 to 175 | $^\circ C$ |

**GL Silicon N-Channel Power MOSFET****Electrical Characteristics** ($T_c = 25^\circ\text{C}$ unless otherwise specified) :**OFF Characteristics**

| Symbol | Parameter | Test Conditions | Rating | | | Units |
|--------------|-----------------------------------|--|--------|------|------|---------|
| | | | Min. | Typ. | Max. | |
| V_{DSS} | Drain to Source Breakdown Voltage | $V_{GS}=0V, I_D=250\mu A$ | 100 | -- | -- | V |
| I_{DSS} | Drain to Source Leakage Current | $V_{DS}=100V, V_{GS}=0V, T_a=25^\circ\text{C}$ | -- | -- | 1.0 | μA |
| $I_{GSS(F)}$ | Gate to Source Forward Leakage | $V_{GS}=+20V$ | -- | -- | 0.1 | μA |
| $I_{GSS(R)}$ | Gate to Source Reverse Leakage | $V_{GS}=-20V$ | -- | -- | -0.1 | μA |

ON Characteristics^{a3}

| Symbol | Parameter | Test Conditions | Rating | | | Units |
|--------------|-------------------------------|-------------------------------|--------|------|------|------------|
| | | | Min. | Typ. | Max. | |
| $R_{DS(ON)}$ | Drain-to-Source On-Resistance | $V_{GS}=10V, I_D=3A$ | -- | 210 | 240 | m Ω |
| $V_{GS(TH)}$ | Gate Threshold Voltage | $V_{DS}=V_{GS}, I_D=250\mu A$ | 1.2 | -- | 2.5 | V |

Pulse width $t_p \leq 380\mu s, \delta \leq 2\%$ **Dynamic Characteristics^{a4}**

| Symbol | Parameter | Test Conditions | Rating | | | Units |
|-----------|------------------------------|--|--------|------|------|-------|
| | | | Min. | Typ. | Max. | |
| g_{fs} | Forward Transconductance | $V_{DS}=5V, I_D=1A$ | 1 | -- | -- | S |
| C_{iss} | Input Capacitance | $V_{GS}=0V, V_{DS}=50V$ $f=1.0\text{MHz}$ | -- | 190 | -- | pF |
| C_{oss} | Output Capacitance | | -- | 22 | -- | |
| C_{rss} | Reverse Transfer Capacitance | | -- | 13 | -- | |

Resistive Switching Characteristics^{a4}

| Symbol | Parameter | Test Conditions | Rating | | | Units |
|--------------|-----------------------------------|---|--------|------|------|-------|
| | | | Min. | Typ. | Max. | |
| $t_{d(ON)}$ | Turn-on Delay Time | $V_{DD}=50V, I_D=1.3A, R_L=39\Omega$ $V_{GS}=10V, R_G=1\Omega$ | -- | 6 | -- | ns |
| t_r | Rise Time | | -- | 10 | -- | |
| $t_{d(OFF)}$ | Turn-Off Delay Time | | -- | 10 | -- | |
| t_f | Fall Time | | -- | 6 | -- | |
| Q_g | Total Gate Charge | $V_{DD}=50V, I_D=1.3A$ $V_{GS}=10V$ | -- | 5.2 | -- | nC |
| Q_{gs} | Gate to Source Charge | | -- | 0.75 | -- | |
| Q_{gd} | Gate to Drain ("Miller") Charge | | -- | 1.4 | -- | |

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| Source-Drain Diode Characteristics | | | | | | |
|------------------------------------|--|-----------------------|--------|------|------|-------|
| Symbol | Parameter | Test Conditions | Rating | | | Units |
| | | | Min. | Typ. | Max. | |
| I_S | Continuous Source Current ^{a2} (Body Diode) | | -- | -- | 2 | A |
| V_{SD} | Diode Forward Voltage ^{a3} | $I_S=1.3A, V_{GS}=0V$ | -- | -- | 1.2 | V |

| Symbol | Parameter | Typ. | Units |
|-----------------|-----------------------------------|------|-------|
| $R_{\theta JA}$ | Junction-to-Ambient ^{a2} | 100 | °C/W |

^{a1} : Repetitive Rating: Pulse width limited by maximum junction temperature.

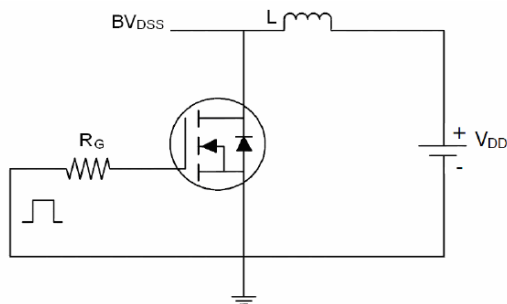
^{a2} : Surface Mounted on FR4 Board, $t \leq 10\text{sec}$.

^{a3} : Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

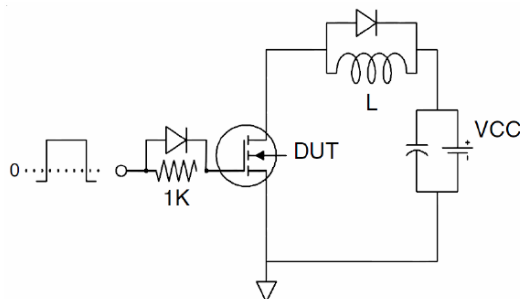
^{a4} : Guaranteed by design, not subject to production

Test circuit

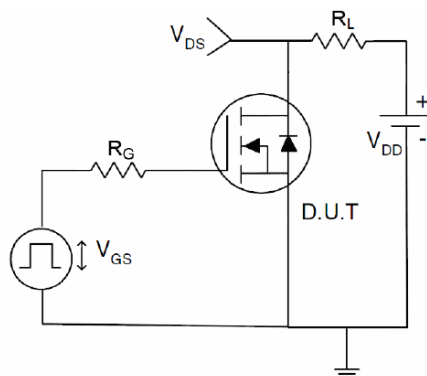
1) EAS test Circuit

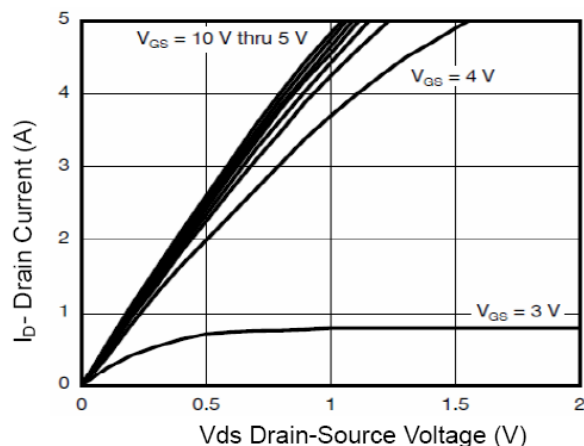
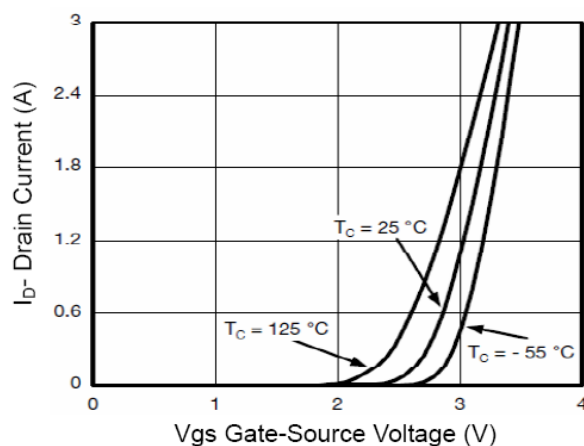
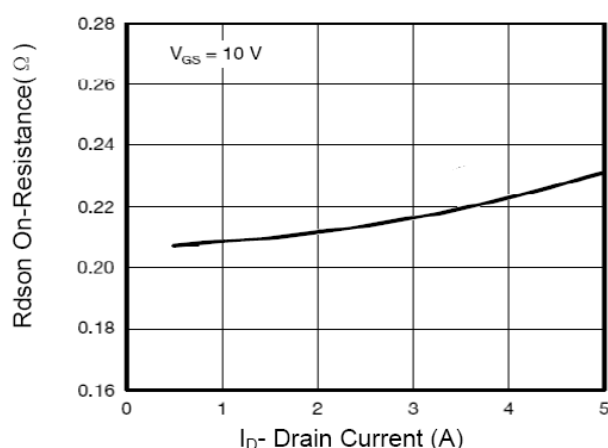
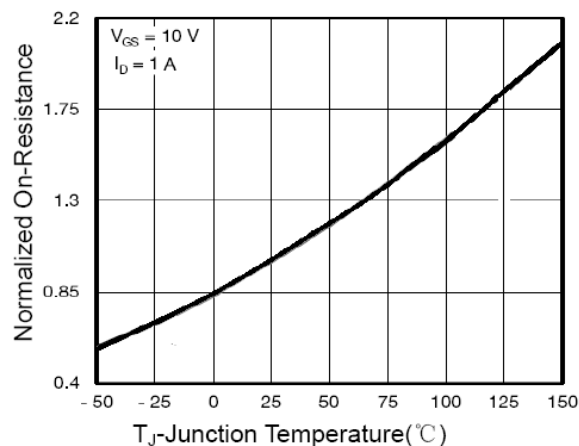
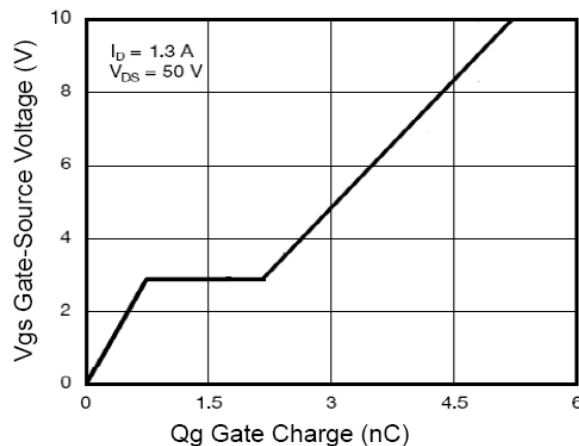
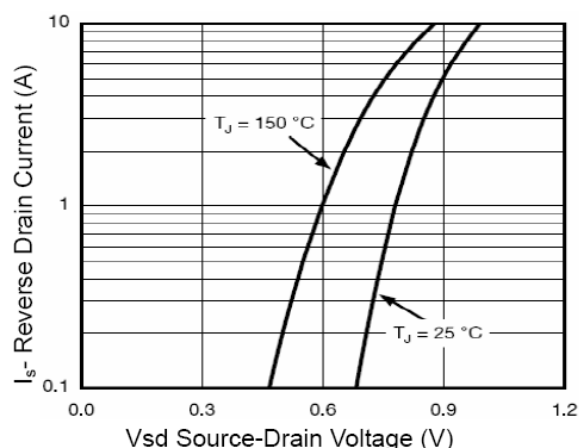


2) Gate charge test Circuit



3) Switch Time Test Circuit



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Characteristics Curve :

Figure 1 Output Characteristics

Figure 2 Transfer Characteristics

Figure 3 $R_{DS(on)}$ - Drain Current

Figure 4 $R_{DS(on)}$ -Junction Temperature

Figure 5 Gate Charge

Figure 6 Source- Drain Diode Forward

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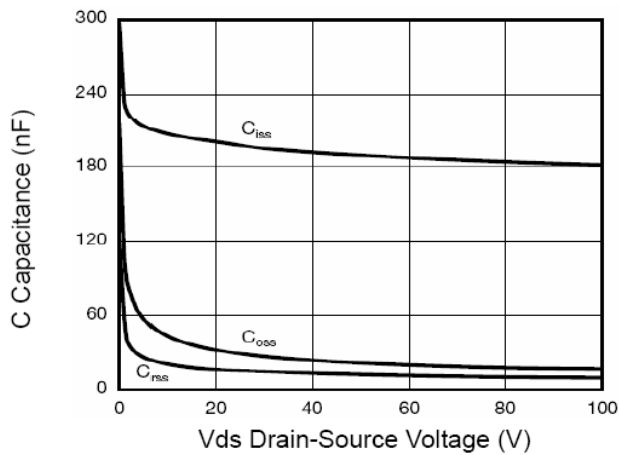


Figure 7 Capacitance vs Vds

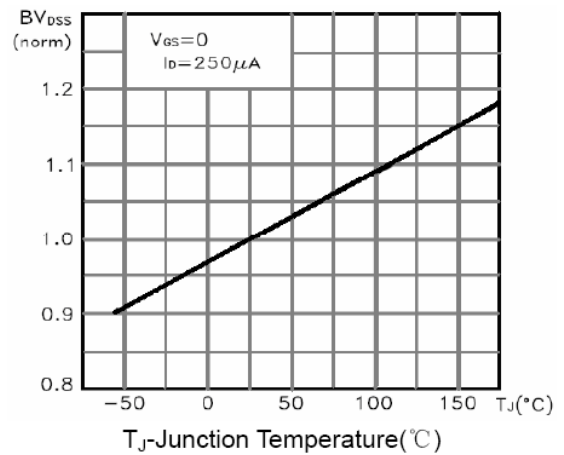


Figure 9 BV_{DSS} vs Junction Temperature

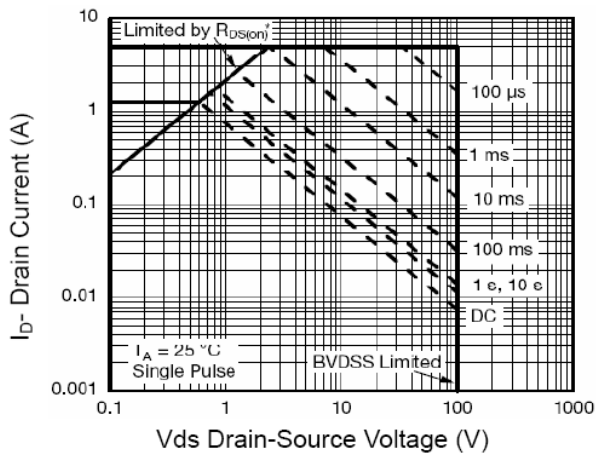


Figure 8 Safe Operation Area

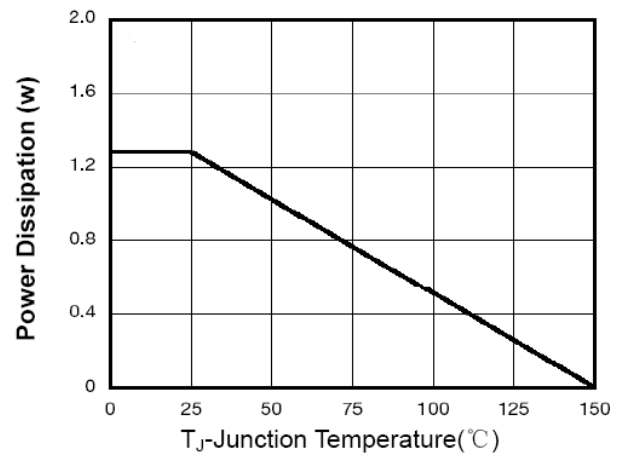


Figure 10 Power De-rating

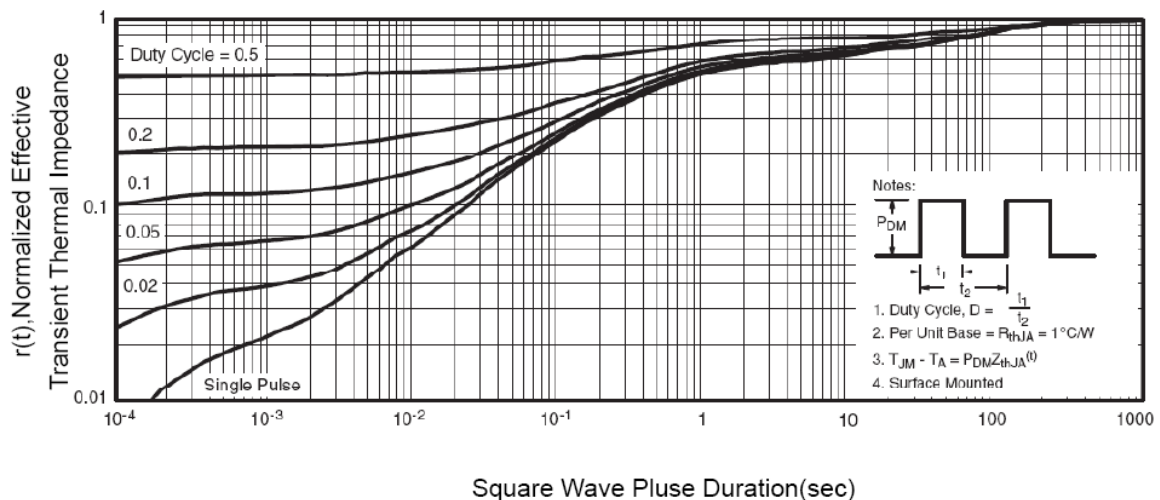


Figure 11 Normalized Maximum Transient Thermal Impedance

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